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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/828,610	04/21/2004	Noel B. Dequina	INSL.0101	5197
26122	7590	05/20/2005		
LAW OFFICES OF GARY R. STANFORD 330 W OVERLOOK MOUNTAIN RD BUDA, TX 78610			EXAMINER LAM, TUAN THIEU	
			ART UNIT 2816	PAPER NUMBER

DATE MAILED: 05/20/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	10/828,610	DEQUINA ET AL.	
	Examiner	Art Unit	
	Tuan T. Lam	2816	
<i>-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --</i>			
<b>Period for Reply</b>			
<b>A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.</b>			
<ul style="list-style-type: none"> <li>- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.</li> <li>- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.</li> <li>- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.</li> <li>- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).</li> </ul>			
<b>Status</b>			
1) <input checked="" type="checkbox"/> Responsive to communication(s) filed on <u>24 May 2004</u> .			
2a) <input type="checkbox"/> This action is <b>FINAL</b> .		2b) <input checked="" type="checkbox"/> This action is non-final.	
3) <input type="checkbox"/> Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.			
<b>Disposition of Claims</b>			
4) <input checked="" type="checkbox"/> Claim(s) <u>1-27</u> is/are pending in the application.			
4a) Of the above claim(s) _____ is/are withdrawn from consideration.			
5) <input checked="" type="checkbox"/> Claim(s) <u>1,2 and 4-8</u> is/are allowed.			
6) <input checked="" type="checkbox"/> Claim(s) <u>9-11,17,19,20 and 24-27</u> is/are rejected.			
7) <input checked="" type="checkbox"/> Claim(s) <u>3,12-16,18 and 21-23</u> is/are objected to.			
8) <input type="checkbox"/> Claim(s) _____ are subject to restriction and/or election requirement.			
<b>Application Papers</b>			
9) <input type="checkbox"/> The specification is objected to by the Examiner.			
10) <input checked="" type="checkbox"/> The drawing(s) filed on <u>24 May 2004</u> is/are: a) <input checked="" type="checkbox"/> accepted or b) <input type="checkbox"/> objected to by the Examiner.			
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).			
11) <input type="checkbox"/> The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.			
<b>Priority under 35 U.S.C. § 119</b>			
12) <input type="checkbox"/> Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).			
a) <input type="checkbox"/> All    b) <input type="checkbox"/> Some * c) <input type="checkbox"/> None of:			
1. <input type="checkbox"/> Certified copies of the priority documents have been received.			
2. <input type="checkbox"/> Certified copies of the priority documents have been received in Application No. _____.			
3. <input type="checkbox"/> Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).			
* See the attached detailed Office action for a list of the certified copies not received.			
<b>Attachment(s)</b>			
1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)		4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.	
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)		5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)	
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.		6) <input type="checkbox"/> Other: _____.	

## **DETAILED ACTION**

### ***Claim Objections***

1. Claim 3 is objected to because of the following informalities: the recitation of “said first and second current amplifiers” lacks proper antecedent basis. Did applicant mean first and second current mirror amplifiers?. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 102***

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 9, 17, 19, 20 and 24-27 are rejected under 35 U.S.C. 102(b) as being anticipated by Ozguc (USP 6,037,811).

Figure 1 of Ozguc shows a driver circuit comprising a P and N channel transistors (MP1, MN1) coupled together at an intermediate node (OUT) and in between voltage source Vcc and ground, a multi-level current pulse generator provides at least one multi-level current sufficient to switch said P and N channel while minimizing average power dissipation (variable currents I1 and I2 are capable of generating current adjustably at different levels, lower level current dissipates less power than high level current, thus having a minimum average power dissipation), a clamp circuit (128 and 158) coupled to limit gate to source voltage of P channel and N channel devices to a predetermined maximum level of Vcc as called for in claims 9, 19.

Regarding claims 17 and 25, the first current pulse I1 is to turn on the P channel transistor (charging) while the N channel is off, the second pulse I2 is to turn off P channel transistor while the N channel is on (discharging).

Regarding claim 20, the adjustable currents I1 and I2 are switchable current sources.

Regarding claim 24, current mirror circuits are seen as 222 and 252 of figure 2.

Regarding claim 26, the first current pulse I1 generated in response to the assertion of PWM (106) and the second current pulse I2 generated in response to the dissertation of the PWM (108).

Regarding claim 27, a first clamping circuit (124) of Ozguc prevents said of PDMOS transistor from dropping below a first voltage level (Vcc) and a second clamping circuit (158) prevents said gate of said NDMOS transistor from rising above a second voltage level (ground).

#### ***Claim Rejections - 35 USC § 103***

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 10 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ozguc (USP 6,073,811) in view of Doutreloigne (USP 6,731,151) Figure 1 of Ozguc discloses all the limitation of claim 9 as noted above except for the P and N channel devices to be PDMOS and NDMOS, respectively as called for in claim 10. Doutreloigne teaches that PDMOS and NDMOS are known for capable of withstanding a high voltage. Therefore, it would have been

obvious to person skilled in the art at the time the invention was made to replace Ozguc's P and N channel devices (MP1 and MN1) with PDMOS and NDMOS, respectively, for the purpose of preventing the transistors devices breaking down under high voltage operation.

Regarding claim 11, a first clamping circuit (124) of Ozguc prevents said of PDMOS transistor from dropping below a first voltage level (Vcc) and a second clamping circuit (158) prevents said gate of said NDMOS transistor from rising above a second voltage level (ground).

***Allowable Subject Matter***

6. Claims 1-2, 4-8 are presently allowed.
7. Claim 3 is objected to but would be allowable if rewritten to overcome the objection as noted above.
8. Claims 12-16, 18, 21-23 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Conclusion***

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P. CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Tuan T. Lam  
Primary Examiner  
Art Unit 2816

5/17/2005